

#20
Fee Maintenance
1/10/06

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Shrivastava, et al.)
)
Patent No.: 6,392,267)
) Group Art: 2811
Issue Date: 5/21/02)
) Examiner: Sara Crane
Application No.: 08/845,752)
)
Application Date: 4/25/97)
)
For: FLASH EPROM ARRAY WITH)
SELF-ALIGNED SOURCE CONTACTS)
AND PROGRAMMABLE SECTOR)
ERASE ARCHITECTURE)

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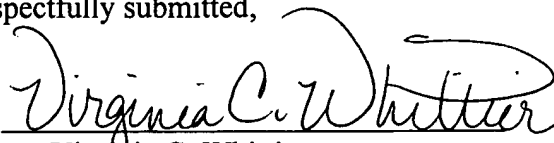
**FEE ADDRESS FOR MAINTENANCE FEE PURPOSES
IN ACCORDANCE WITH 37 C.F.R. §1.363**

In accordance with the provisions of 37 C.F.R. §1.363, the fee address set forth below is being supplied for purposes of receiving notices, receipts, and other correspondence relating to the payment of maintenance fees:

ALLIANCE SEMICONDUCTOR CORPORATION
2575 Augustine Drive
Santa Clara, CA 95054-4900

Respectfully submitted,

By:



Virginia C. Whittier
Alliance Semiconductor Paralegal

Dated: 11/9/05